

RESUME

Current Position : Joint General Manager

Type of organization : Engineering Procurement/projects & Contract

Current responsibilities:

1. Design and Develop programme and content for Training to fresh Graduate Engineers and Diploma Engineers.
2. Laboratory development for above training.
3. Designed extended training program for GETs to keep them engaged in first year leveraging Information Technology for course delivery.
4. Design and Develop new program for existing L&T Employees.
5. Help L&T Public Charitable Trust to setup training center in trade like Electrician/Wiremen, Welder, Fitter and CNC for school drop-outs.
6. Managing the L&T run polytechnic college as Principal.

New initiatives taken:

1. Organizing Technology Conclave for L&T Engineers-pan L&T event.
2. Designed CAD competition for CAD engineers-pan L&T.
3. Initiated skill competition for workmen in L&T.
4. Program for L&T brand building in engineering campus (TLS).
5. e-content for Technical subject through any time learning/blended learning.

Technical Skills:

Assembly language of Intel/ Microchip Microprocessor, Microcontroller, System Design using microprocessor/microcontroller, embedded C, Industrial IoT.

Present Techno-social Engagement:

1. Governing Council member for 2019-20 & 1920-21 of IETE(Institution of Electronics & Telecommunication Engineers)
2. Member of Maharashtra CII skill development & affirmative action panel.
3. Member of western India CII skill development & affirmative action panel.
4. Member of board of studies Electrical department, VJTI Mumbai.
5. Member of Board of studies Mechatronic department, NMIMS University Mumbai.

6. Visiting professor to BITS Pilani, teaching pervasive computing to post graduate students.

Professional membership:

1. Life member of Indian society for technical education (MISTE)
2. Life member of Institute of Telecommunication and Electronics Engineer (FIETE)
3. Life member of Computer society of India (CSI)

Professional experience:

1. Name of the employer : PCS Data Products Ltd. Bombay
Designation : Sr. Hardware Engineer
Date of joining : Feb 1987
Date of Leaving : Mar 1988

Nature of Duties Performed: -

- Design development and testing of test jig for intelligent power supply controller used in minicomputer of Data General (DG) make.
- Testing of 8-inch floppy controller card for DG minicomputer.
- Assembly and Testing of minicomputer.

- 2 . Name of the employer: Minifax Electronics System Pvt. Ltd. Aurangabad.
Designation : Sr. Engineer
Date of joining : Mar 1988
Date of Leaving : Jun 1989

Nature of Duties Performed: -

- Setting up the production line for Manufacturing of PC range of Computers.
- Design and development of various testing procedure for PC range of cards & System.
- Design and development of 4 to 1 printer sharing device.
- Design retrofitting system for normal lathe m/c for Gajra Gears, Indore.

- 3 . Name of the employer: College of Applied Science, Aurangabad.
Designation : Sr. Lecturer
Date of joining : Jun 1989
Date of Leaving : Jun 1992

Nature of Duties Performed:

- Teaching B.E. (Computer), B.E. (Electronics) and M.Sc. (Computer Science) students
- Looking after the Maintenance of Personal computers in the departments.
- Setting up of a new computer lab.

4. **NIELET (Formerly Center for Electronics Design and Technology of India) Aurangabad since July 1992 to July 2006 as Principal Design Engineer. Govt. of India Scientific society.**

Taking care of: -

- Head of Embedded system Design lab. Head of Agriculture instrumentation lab.
- Presently the lab have IDE for Microchip, Motorola, MSP430 and mcs-51 based microcontroller. C cross compiler and tiny RTOS like salvo.
- Project worked on:
 - a. Microcontroller based Home automation system around X10 protocol.
 - b. Microchip controller 16f876 based remote temperature monitoring system for medical application.

7. **Presently working with Larsen & Toubro Limited, Mumbai Since August 2006.**

Patents and Publications: -

- * Published six papers at national level.
- * Published one papers at international level.

Languages: - Marathi, Hindi and English

Visit Abroad: -

Worked with professor P. Renaud Microtechnique Department at Ecole Polytechnique Federale De Lausanne Switzerland (EPFL) on project titled "Digital interfacing of Capacitive Pressure micro sensor" from 1st May 1995 to 31st December 1995 for Eight months.

Personal details :

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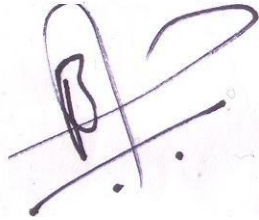
Mail : Bhuwansing@gmail.com

Date Of Birth : 10th Feb 1963

Summary of Qualifications: -

Degree	Year of Passing	University	% of Marks	Sp. Subjects
B.E. (Electronics & Power)	May 1985	Nagpur	68	Microprocessor, Instrumentation, Power
M.E. (Electronics with specialization in Computer)	Dec 1986	Bombay	79	Computer, Medical, electronics, Instrumentation

Above information to correct to the best of my knowledge.



B. A. Damahe